

FIG. 1

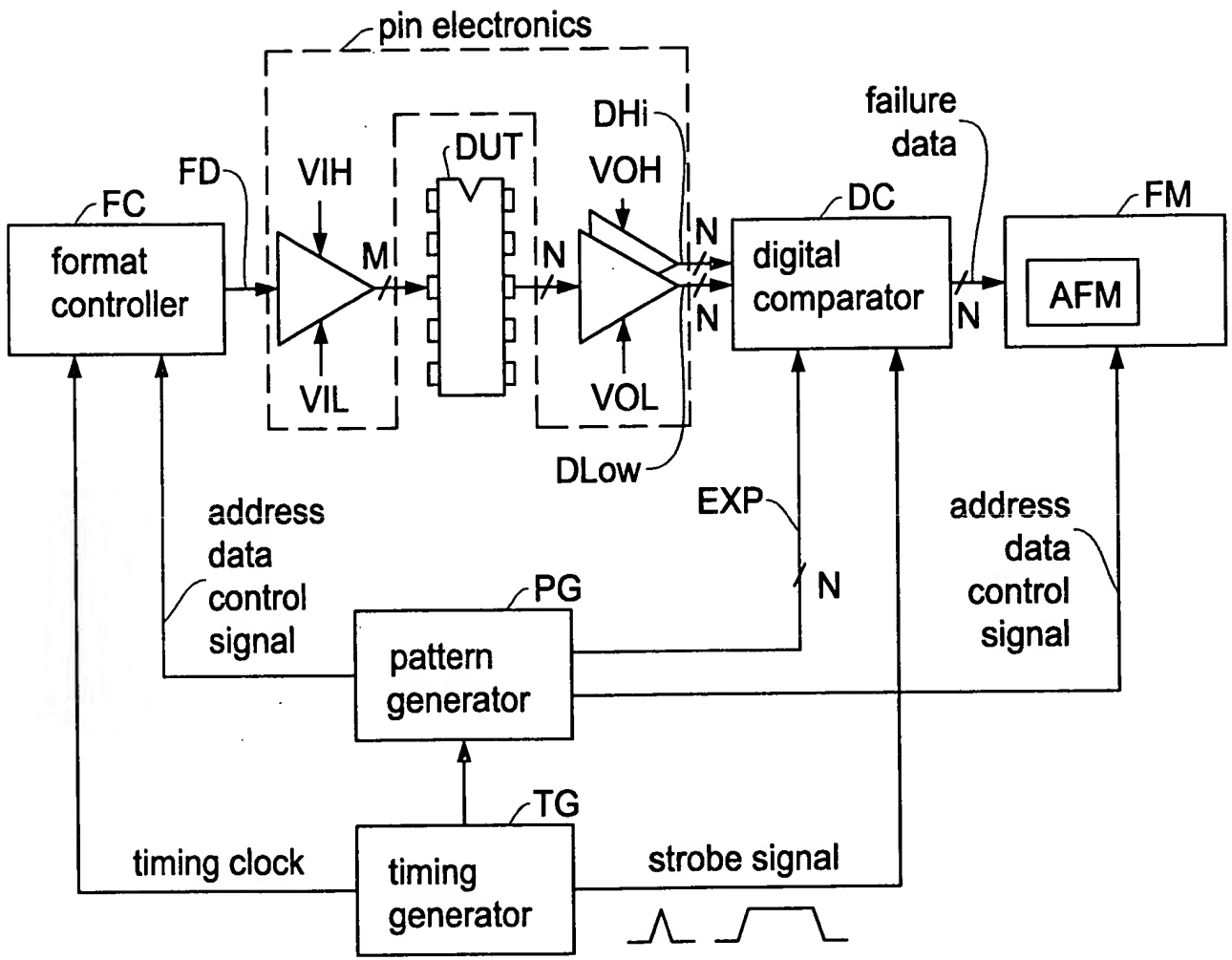


FIG. 2A

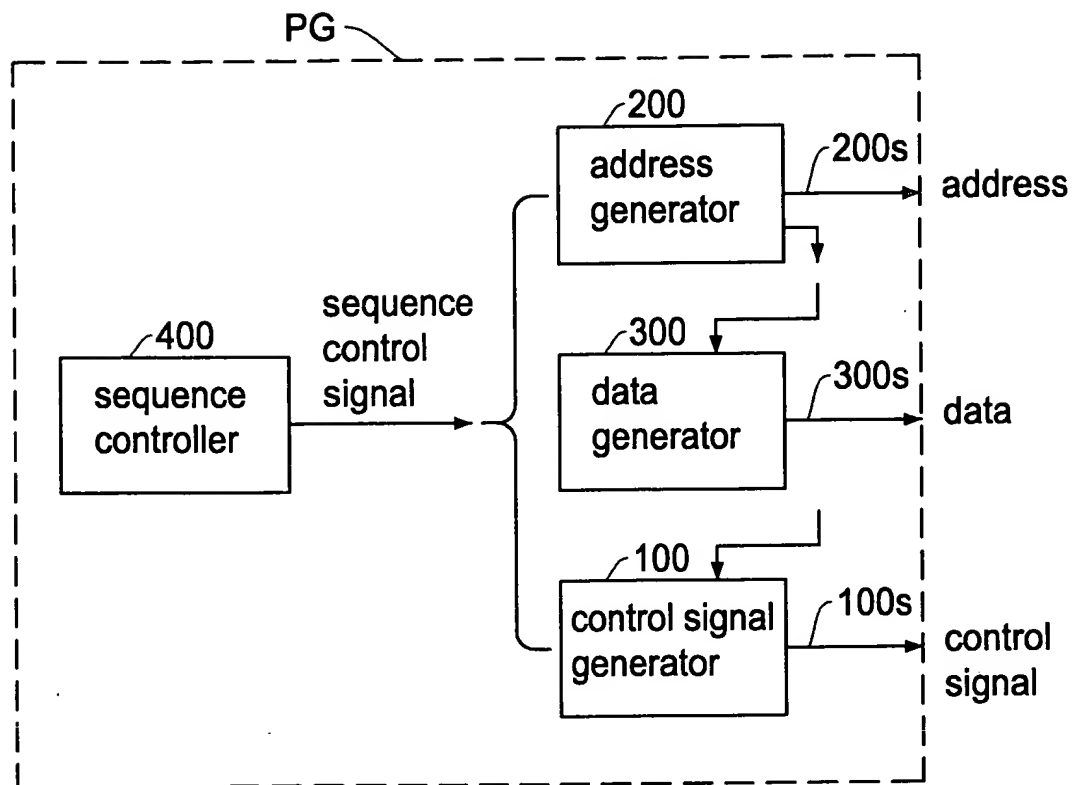


FIG. 2B

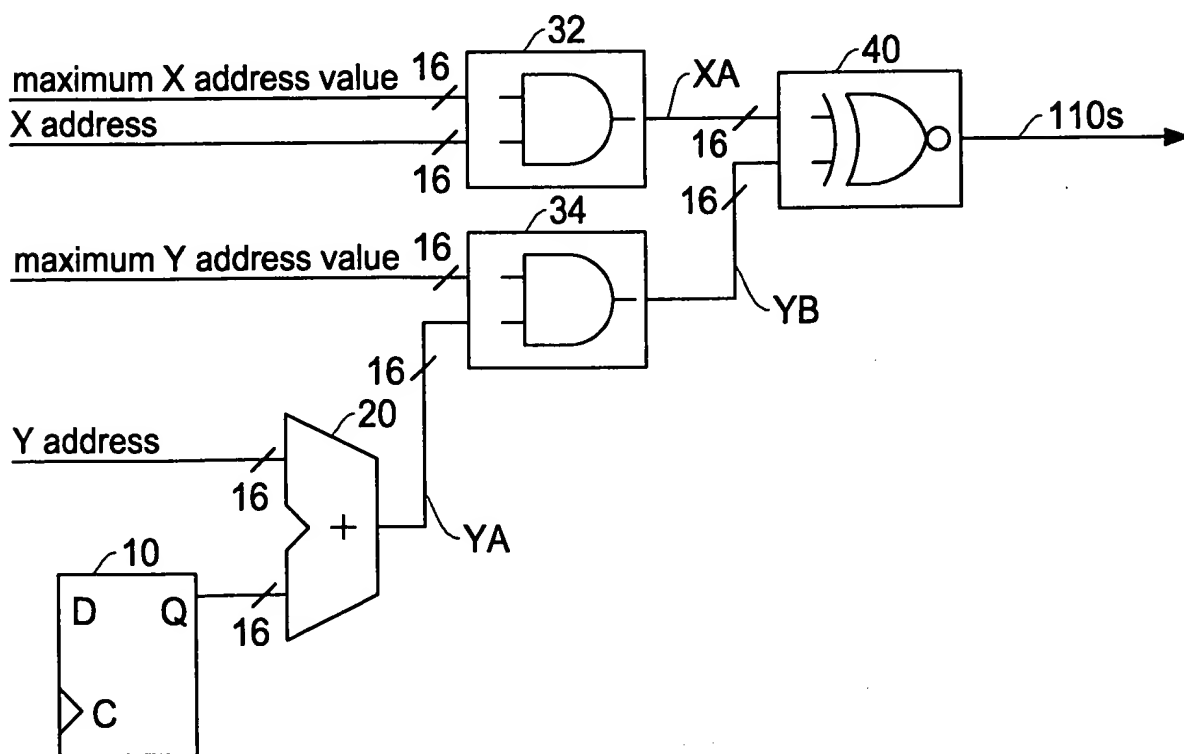


FIG. 3A

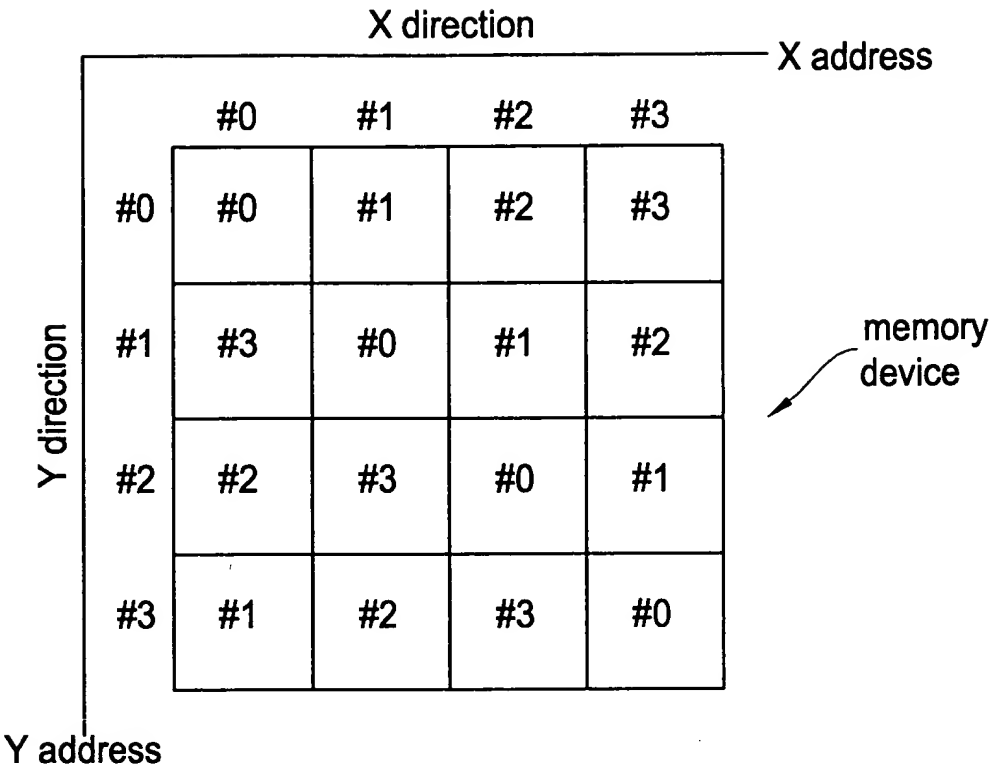


FIG. 3B

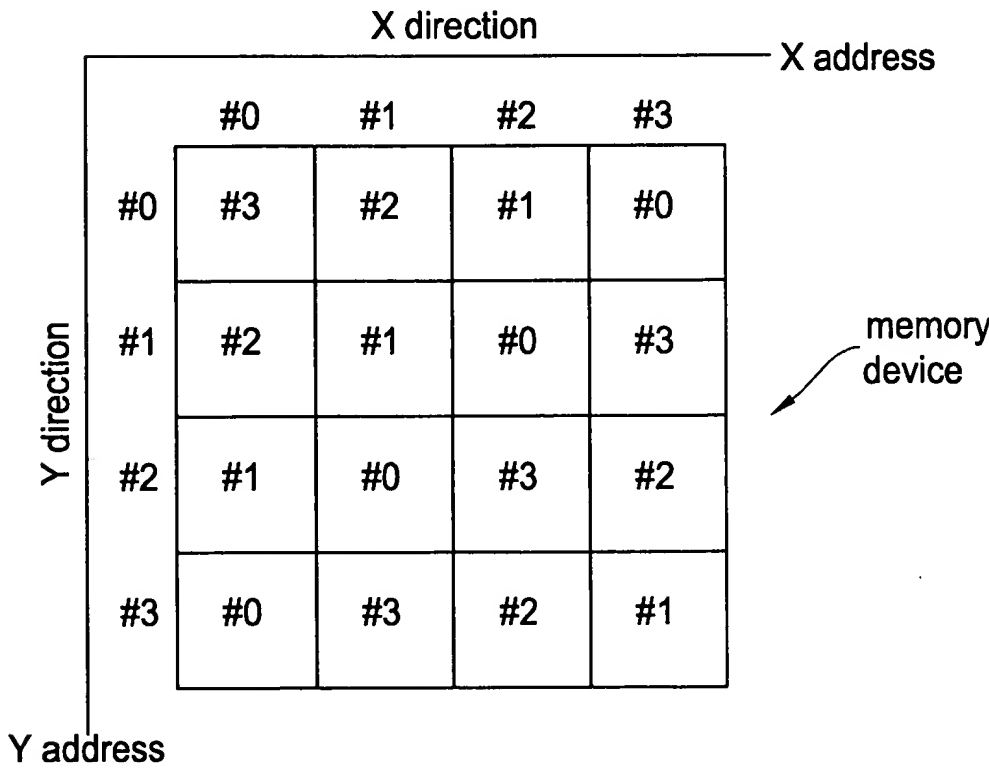


FIG. 4

max. X address value = #3
max. Y address value = #3
diagonal inversion set value = #3

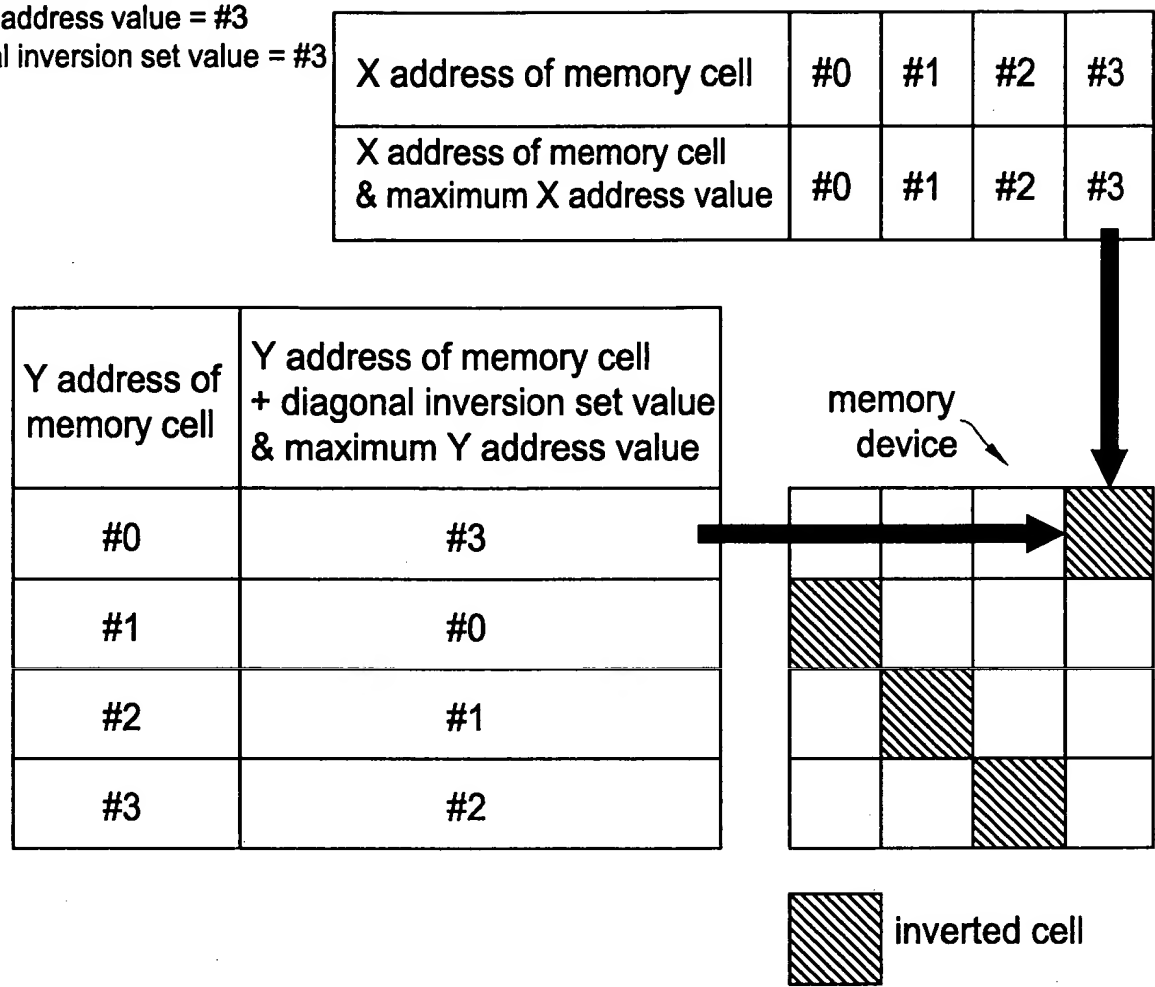


FIG. 5

max. X address value = #7
max. Y address value = #3
diagonal inversion set value = #3

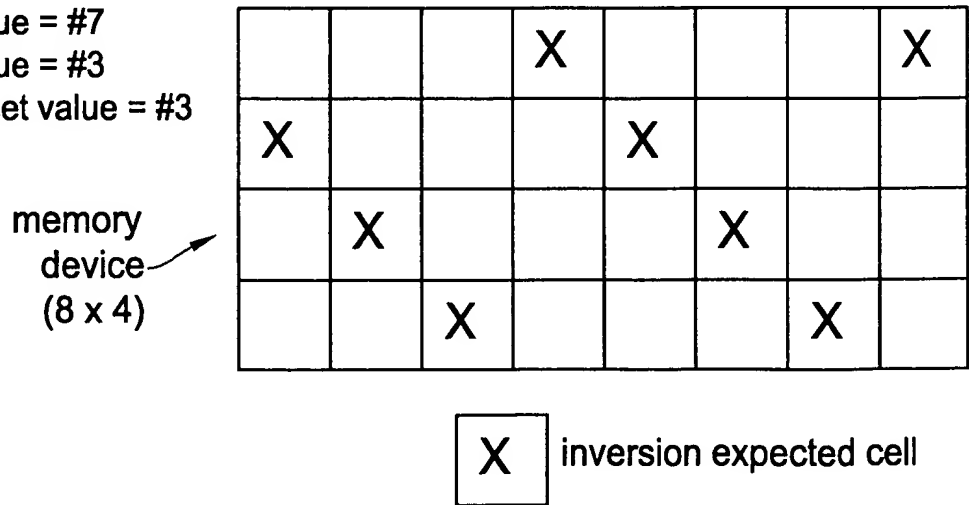
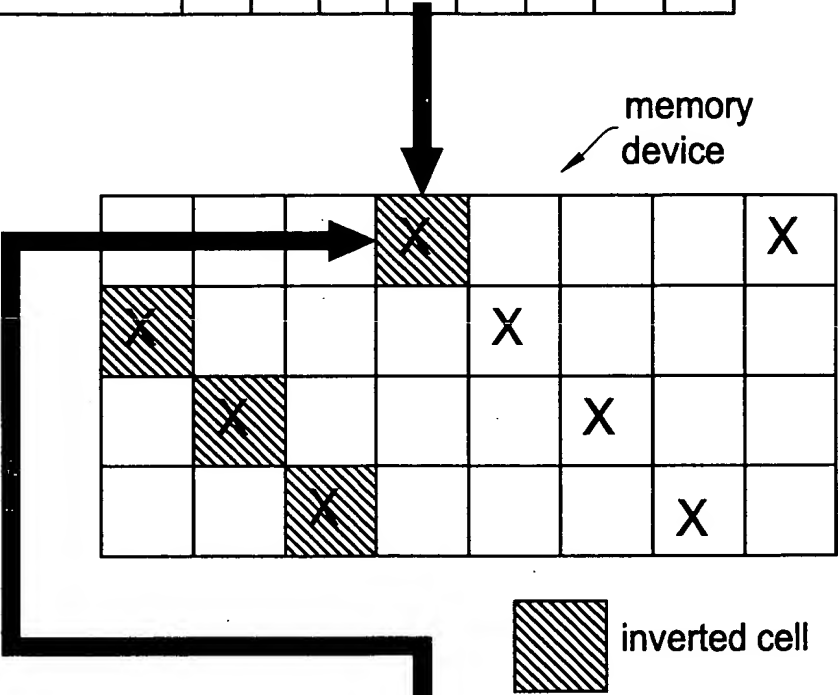


FIG. 6

max. X address value = #7
max. Y address value = #3
diagonal inversion set value = #3

X address of memory cell	#0	#1	#2	#3	#4	#5	#6	#7
X address of memory cell & maximum X address value	#0	#1	#2	#3	#4	#5	#6	#7



Y address of memory cell	Y address of memory cell + diagonal inversion set value & maximum Y address value
#0	#3
#1	#0
#2	#1
#3	#2

FIG. 8

max. X address value = #7
max. Y address value = #3
diagonal inversion set value = #3

